Method for fabricating thin film transistor

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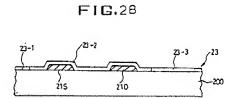
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Abstract of GB2338343

A method of fabricating a thin film transistor comprises crystallizing an amorphous silicon layer 22 having sloping surface and flat surface by SLS technique using a laser beam having predetermined energy density so as to melt the sloping surface as well as the flat surface of the amorphous silicon layer 22 to form a crystallized silicon layer 23 and forming the active layer by selectively etching the crystallized silicon layer. The laser beam is applied non-vertically to the sloping surface while the laser beam is applied vertically to the flat surface. Although the sloping surface and the flat surface of the amorphous silicon layer 22 are irradiated with laser beam having same laser energy density, the absorbed energy density of the sloping surface may be lower than that of the flat surface. The laser beam requires first energy density to substantially melt the sloping surface and second energy density to substantially melt the flat surface of the amorphous silicon 22. The amorphous silicon layer 22 is irradiated with the laser beam having the first energy density to substantially melt both the sloping and flat surfaces.



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